

**REMARKS**

Claims 1-24 are pending in the current application. Of those, claims 1, 11, 21, and 23 are independent claims. Claims 21 and 23 are amended by this Response. No claims are canceled or added by this Response.

Initially, Applicant requests the Examiner's acknowledgement of the claim of foreign priority under 35 U.S.C. § 119 and the Examiner's indication of whether the drawings filed on February 20, 2004 are accepted by the Examiner.

**35 U.S.C. § 112, Second Paragraph – Rejection**

Claims 21-24 stand rejected under 35 U.S.C. § 112, second paragraph, as being indefinite for failing to particularly point out and distinctively claim the subject matter which Applicant regards as the invention. Applicant respectfully traverses this rejection.

The Examiner asserts at page 2 of the current Office Action that “the claims are incomplete in that they recite only a portion of the methodology required for the \*\*carry look-ahead adder\*\* to become operational, i.e., they omit essential elements and/or steps. In particular, the Examiner asserts that “the ‘BLOCK CARRY UNIT’ (Fig. 1, element 120) does not provide the SUM and Carry out values.” Claim 21 is directed to “A carry look-ahead adder” where “the block carry circuit is configured to output the first-type block carry signal, the second-type block carry signal, and the third-type block carry signal.” Applicant respectfully submits that claim 21 contains all of the elements and/or steps necessary for a carry look-ahead adder to output “the first-type block carry signal,” “the second-type block carry signal,” and “the third-type block carry signal” as required by claim 21. Further, Applicant notes that claim 21 does not recite “SUM and carry out values” as asserted above by the Examiner. Therefore, Applicant respectfully submits that claim 21 contains all the elements necessary for a carry look-

ahead adder configured to output the first-type block carry signal, the second-type block carry signal, and the third-type block carry signal.

Claim 23 is amended recite to “A summation method for carry look-ahead addition performed by a carry look-ahead adder” including “outputting the carry propagation bit values, the carry kill bit values, the first-type block carry signal, the second-type block carry signal, and the third-type block carry signal.” Similar to claim 21, Applicant respectfully submits that claim 23 contains all of the steps necessary for a summation method for carry look-ahead addition performed by a carry look-ahead adder to output “the carry propagation bit values,” “the carry kill bit values,” “the first-type block carry signal,” “the second-type block carry signal,” and “the third-type block carry signal” as required by claim 23, and that claim 23 does not recite “SUM and carry out values” as asserted above by the Examiner.

Applicant respectfully submits that claims 21 and 23 particularly point out and distinctly claim the subject matter which Applicant regards as the invention. Accordingly, Applicant respectfully requests the rejections to claims 21 and 23, and the claims depending therefrom, under 35 U.S.C. § 112, second paragraph, be withdrawn.

### **35 U.S.C. § 101 – Rejection**

Claims 1-24 stand rejected under 35 U.S.C. § 101 because the Examiner asserts the claimed invention is directed to non-statutory subject matter. Applicant respectfully traverses this rejection.

Claim 1 recites “a summation selection circuit...**to output a final sum result.**” Therefore, Applicant respectfully submits that claim 1 discloses a practical application with a concrete, useful, and tangible result, i.e., the final sum result. For example, example embodiments in the specification at least at paragraph [0055] disclose that a final sum result may

be used to represent the summation of an addend and an augend within one phase of a clock signal at a high speed, and that a final sum result may be output as a tangible result.

Claim 11 recites “A summation method for carry look-ahead addition performed by a carry look-ahead adder” including “outputting the final sum result,” and therefore, claim 11 discloses a practical application with a concrete, useful, and tangible result for somewhat similar reasons as discussed above in regards to claim 1.

Claim 21 recites “A carry look-ahead adder...configured **to output the first-type block carry signal, the second-type block carry signal, and the third-type block carry signal.**” Therefore, Applicant respectfully submits that claim 1 discloses a practical application with a concrete, useful, and tangible result, i.e., the first-type block carry signal, the second-type block carry signal, and the third-type block carry signal. For example, example embodiments in the specification at least at FIG. 1 and at paragraph [0020] disclose that output the first-type block carry signal, the second-type block carry signal, and the third-type block carry signal may be used as input for a Control Circuit 140, and example embodiments in the specification at least at paragraph [0029] disclose selection control signals CINH and CINL may be based on the output first-type block carry signal, second-type block carry signal, and third-type block carry signal.

Claim 23 recites “A summation method for carry look-ahead addition performed by a carry look-ahead adder” including “outputting the carry propagation bit values, the carry kill bit values, the first-type block carry signal, the second-type block carry signal, and the third-type block carry signal.” Therefore, Applicant respectfully submits that claim 23 discloses a practical application with a concrete, useful, and tangible result for somewhat similar reasons as discussed above in regards to claim 21.

Applicant notes that should the Examiner have any further suggestions for amending the claims in view of the rejections under 35 U.S.C. §101, Applicant would be willing to consider the Examiner's suggestions in the interests of forwarding prosecution.

Accordingly, Applicant respectfully requests the rejections to claims 1, 11, 21, and 23, and the claims depending therefrom, under 35 U.S.C. §101 be withdrawn.

### CONCLUSION

Accordingly, in view of the above amendments and remarks, reconsideration of the objections and rejections and allowance of each of the claims in connection with the present application is earnestly solicited.

Should there be any outstanding matters that need to be resolved in the present application, the Examiner is respectfully requested to contact John A. Castellano at the telephone number of the undersigned below.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 08-0750 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

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By

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